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Complete if Known

Application Number	09/973,429
Filing Date	10-09-2001
First Named Inventor:	Grochowski, et al.
Art Unit	2183
Examiner Name	Aimee J. Li
Attorney Docket Number	042390.P4899C

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
AL		DULONG, ET AL., "An Overview of the Intel® IA-64 Compiler", Intel Technology Journal Q4, 1999, Pages 1-15.	
AL		Intel® Itanium™ Architecture Software Developer's Manual, Part II: Optimization Guide for the Intel® Itanium™ Architecture, Predication, Control Flow, and Instruction Stream, Volume 1: Application Architecture, Revision 2.0, December 2001, Pages 1:129, 1:155 - 1:163.	
AL		Intel® Itanium™ Processor, Product Highlights, Copyright © 2001, Intel Corporation.	
AL		TOM R. HALFHILL, "Beyond Pentium II", BYTE.com, December 1997, Pages 1-10.	
AL		JIM TURLEY, "64-Bit CPUs: What you Need to Know", Extreme Tech, February 8, 2002, Pages 1-12.	
AL		NICHOLAS P. CARTER, ECE 412-Advanced Computer Architecture, Lecture 4: Predication, September 13, 2000, Pages 1-17.	
AL		PNEVMATIKATOS ET AL., "Guarded Execution and Branch Prediction in Dynamic ILP Processors", Proceedings of the 21 st Annual International Symposium on Computer Architecture, IEEE, pp. 120-129.	
AL		MAHLKE ET AL., "Characterizing the Impact of Predicated Execution Branch Prediction", Proceedings of the 27 th Annual International Symposium Microarchitecture, Micro-27, IEEE, Nov. 30-Dec. 2, 1994, pp. 217-227.	

Examiner Signature	Aimee Li	Date Considered	6-20-2005
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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